

DAG 7.4S



The Endace DAG[®] 7.4S network monitoring card provides the flexibility to capture and inspect traffic on SONET (SDH) OC-3c/12c/48c (STM-1c/4c/16c) Packet-Over-SONET and ATM networks with 8-lane PCI Express compatibility.

With an efficient interrupt-free and zero-copy direct memory access (DMA) data path, the DAG 7.4S delivers the power to see all.

In addition to interrupt-free and zero-copy DMA, Endace-developed Enhanced Packet Processing (EPP) functionality increases packet processing performance via hardware-based filtering and load-balancing techniques. EPP effectively accelerates application performance by offloading common features into hardware and ultimately freeing up more CPU resources for packet processing applications.

On high-speed SONET or ATM networks with 8-lane PCI Express compatibility, the DAG 7.4S delivers the **power to see all** to your applications.

KEY FEATURES

Dual port network monitoring card utilizing small form factor (SFP) optical transceivers supporting:

- Concatenated OC-3c/12c/48c or STM-1 VC4c, STM-4 VC4-4c, STM-16 VC4-16c
- Support Packet over SONET, ATM (OC3c/12c only) and RAW SONET frames capture
- 100% packet capture into host memory at full line rate for all packets from 48 to 9600 bytes (including jumbo frames)
- Full packet or set length packet capture configurable for each memory buffer
- Support for up to 32 memory buffers for load balancing in multi-core host architecture
- Support for 64 hardware-based classification rules for on-board steering and duplication
- Easy integration into existing server hardware with 8-lane PCIe Gen1 bus technology
- Relative timed data replay option enables precise reproduction of traffic conditions.



DAG 7.4S – Technical Specifications

Monitoring interfaces	2x SFP transceivers
Network type	SONET OC3c/OC12c/OC48c SDH STM-1 VC4c SDH STM-4 VC4-4c SDH STM-16 VC4-16c
Packet encapsulations	Packet over SONET (PoS) ATM (OC-3c/12c only) Raw Frames
Hardware packet processing	Enhanced Packet Processing v2
Time synchronization	External: RJ45 connector for RS-422 PPS and IRIG-B signal from GPS, CDMA or TDS Internal: Host PC clock Other DAG cards
Packet timestamping	7.5ns
Pci interface	x8 lane PCI Express Gen 1.1
Operating system supported	Endace software is supported on the following operating systems: Linux and FreeBSD
Power requirements	Less than 20W
Operating temperature	32 to 131°F (0 to 55°C)
Airflow requirements	200 LFM (@50°C Ambient)
Operating humidity	5 to 95% non-condensing
Physical dimensions	Half-height, Half-Length Height: 2.53" (64.25mm) Length: 6.6" (167.5mm)

Companion Products

The following products are compatible with the DAG 7.4S:

Transceivers

OC-3/STM-1 and OC-12/STM-4 Multi-mode 1310nm optical SFP transceiver with LC-type connectors	TXR-OC3/OC12 MM
OC-3/STM-1 and OC-12/STM-4 Single-mode 1310nm optical SFP transceiver with LC-type connectors	TXR-OC3/OC12 SM
OC-48/STM-16 Single-mode 1310nm optical SFP transceiver with LC-type connectors	TXR-OC48 SM
OC-3/STM-1, OC-12/STM-4 and OC-48/STM-16 Single-mode 1550nm optical SFP transceiver with LC-type connectors	TXR-OC3/OC12/OC48 SM

Time Measurement Accessories

Trimble Acutime™ Gold GPS receiver	GPS-2
EndRun Technologies Præcis Ct™ CDMA receiver	CDMA-1
Endace 2-port Time Distribution Server, accepts serial input from GPS/CDMA sources	TDS-2
Endace 6-port expansion module for TDS-2, shares common reference time source	TDS-6
Endace 24-port Time Distribution Server, accepts serial input from GPS/CDMA sources	TDS-24

For information about Taps, Switches, and Cables visit endace.com/accessories

For more information on Endace products visit: endace.com
For enquiries email: enquiries@endace.com